MATRIX DISPLAY BOARD

Patent number:

JP5315653

Publication date:

1993-11-26

Inventor:

SAWA HIROAKI; others: 03

Applicant:

DENKI KAGAKU KOGYO KK

Classification:

- international:

H01L33/00

- european:

Application number:

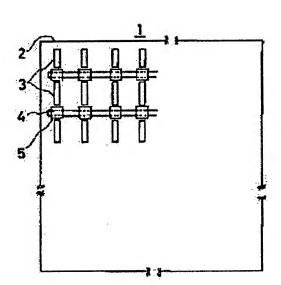
JP19920142002 19920508

Priority number(s):

Abstract of JP5315653

PURPOSE:To provide a matrix display board which improves reliability on connection and reduces the cost and besides is excellent in heat radiation property by using a matrix circuit board without a through hole.

CONSTITUTION:This is a matrix display board where anode circuits 3 and 4 and cathode circuits 4 and 3 are formed in orthogonal directions and besides, with either or both of them cut at the crossing of both, at one side of the printed board 2 and the cut section is connected by the three-dimensional crossing using the metallic frame of a light emitting diode lamp.



Data supplied from the esp@cenet database - Patent Abstracts of Japan